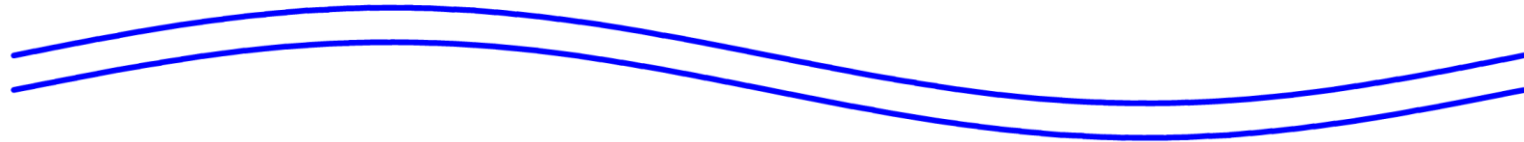


# ***A Continuum of Solutions™***



**Training→Advice→Support→Design→Production**

**FPGAs • SoCs • Boards • Systems**

**Aggressive Schedules • Rock Solid Service and Quality • High Performance**

**A Small Business Partner with a Big Reputation**

# **Bottom Line** *Technologies*

***A Continuum of Solutions***

Training→Advice→Support→Design→Production

**Who is Bottom Line?**

**Small Business**

**Commercial, Government, DoD/IC Clients**

**Broad Technical Expertise and Experience with leading/bleeding edge technologies**

**An Engineering Design Services Firm**

**25 year Track record of High Quality, Leading Edge, On-time Results**



**Columbia MD Training Center**



**Ellicott City MD Design Center**

**BLT helped Xilinx® develop the Design Partner Program in 1990**

<b>Premier Members</b> (0 Design Partners)	<b><u>Select providers</u></b> ✓ <b>BLT</b> 320 point on-site Audit of technical, business, quality, and support processes
<b>Certified Members</b> (Only 7 U.S. Design Partners)	<b><u>Certified level of FPGA expertise</u></b> ✓ <b>BLT</b> Corporate Self-Audit ✓ <b>BLT</b> Certification Testing Passed
<b>Members</b>	<b><u>Broad base of qualified providers</u></b> ✓ <b>BLT</b> XAP Application & Agreement ✓ <b>BLT</b> Business/Technical Sponsor

**As a Certified Member BLT has**

Demonstrated qualified expertise on the latest Xilinx devices and implementation techniques

BLT's engineers pass the same rigorous training used by Xilinx Field Application Engineers worldwide

As Xilinx's exclusive regional Authorized Training Provider, New Alliance candidates must be trained by BLT to become certified

**Bottom**  
**Line** *Technologies*

***A Continuum of Solutions***

Training→Advice→Support→Design→Production

**Training Services**



| AUTHORIZED TRAINING PROVIDER

**BLT is Xilinx's EXCLUSIVE training resource from Rochester, NY through Roanoke, VA**

**MANY HUNDREDS of engineers Trained over 15 years**

**25 Language and FPGA / SoC classes**

**Public Classes**

**Private Classes (on-site / off-site)**

**BLT Professional Advisory Services**

Most every engineer knows that decisions made (or not made) "up-front" usually set the tone and likelihood of a project's success and can have significant impact on schedules and budgets.

BLT Professional Advisory services are most often employed in the early stages of a design and best before entry has started. In addition to IP and device selection and utilization, typical involvements include providing recommendations and best practices for planning, architecting, designing, implementing and verifying designs.

**BLT Professional Support Services**

BLT Professional Support can be used when specific challenges or problems are encountered in the course of entering, implementing or verifying a design. BLT Professional Support provides reactive support that focuses on a specific problem, error message, or functionality that is not working as expected, desired or required.

**Design Services**

**BLT designs FPGAs, SoCs, Boards, and Complete Systems**

**Commercial / Industrial / Medical / Military / IC**

**Low Power • High-Speed • High Density**

**Data and Telecommunications • Digital Signal Processing (DSP)**

**Embedded processors / ARM**

**Single Chip Cryptography • Partial Reconfiguration • High-Speed Signal Integrity**

<b>LARGE Designs?</b> <ul style="list-style-type: none"><li>• Multiple IP cores?</li><li>• High I/O counts?</li><li>• Multiple engineer teams?</li></ul>	<b>FAST Designs?</b> <ul style="list-style-type: none"><li>• What are the BEST WAYS to consistently achieve reliable designs that run at 200, 300 and over 400 MHz?</li></ul>	<b>Cost Sensitive Designs?</b> <ul style="list-style-type: none"><li>• Would fitting into smaller and/or slower parts help?</li></ul>
<b>Inconsistent results?</b> <ul style="list-style-type: none"><li>• Sometimes it routes, sometimes it doesn't?</li><li>• Sometimes meets timing, sometimes it doesn't?</li><li>• Sometimes it simulates and routes but fails on the bench?</li></ul>	<b>Tight Schedules?</b> <ul style="list-style-type: none"><li>• Concerned about issues that might come up late in the development schedule?</li><li>• Pressured to compress schedules more than ever?</li></ul>	<b>Tool Issues?</b> <ul style="list-style-type: none"><li>• Sometimes it seems the tools work against not with you?</li><li>• Sometimes wonder what the tools are REALLY doing?</li><li>• Last minute / simple changes break working designs?</li></ul>

**BLT techniques allow our Clients to achieve breakthrough performance.**

**Designs by BLT and engineers taught by BLT typically shave weeks off each FPGA development schedule.**

**Designs by BLT and engineers taught by BLT are typically implemented with a single click. Can yours?**

**Digital circuitry on a chip**

Communications Systems

Software Defined Radio

Single Chip Cryptography (NSA Approved Type-1)

ULTRA secure applications

Signal Acquisition

Signal Processing (RF / Video / Audio)

Embedded Processing (2000 DSP Elements / Device)

Supercomputing

Reconfigurable computing

Capable of supporting Hi Rel / Rad Hard Applications

**1000+ times faster than software solutions**